## **SAIP2014**



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## Firmware development for the upgrade of the Tile Calorimeter of the ATLAS detector

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## Abstract content <br/> &nbsp; (Max 300 words)<br/> dry-<a href="http://events.saip.org.za/getFile.py/starget="\_blank">Formatting &<br/> &classed chars</a>

At CERN, the European Organization for Nuclear Research, the fundamental structure of the universe is being probed by scientists and engineers. The Large Hadron Collider (LHC) accelerates and collides protons, and also heavy lead ions. The "A Toroidal LHC Apparatus" (ATLAS) is one of two general purpose detectors used for detecting the sub-atomic particles produced during these high-energy collisions. The Tile Calorimeter (TileCal) is the central hadronic calorimeter of the ATLAS detector. The year 2022 has been scheduled to see an upgrade of the LHC in order to increase its instantaneous luminosity. The High Luminosity LHC, also referred to as upgrade Phase-II, means an inevitable complete re-design of the read-out electronics in the TileCal. The completed new read-out architecture is expected to have the front-end electronics transmit full digitized information of the full detector to the back-end electronics system. The back-end system will, thus, provide digital calibrated information with greater precision and granurality to the first level trigger, thereby resulting in improved trigger efficiencies. An evaluation of this new proposed architecture will be carried out in the demonstrator project in 2014, where a small fraction of the detector (1/256) will be used in real conditions. In Phase II, the current Mobidick4 test bench will be replaced by the next generation test bench for the TileCal super-drawers, the new Prometeo (A Portable ReadOut ModulE for Tilecal ElectrOnics). The Prometeo is designed to certificate the TileCal front-end electronics by performing multiple tests. Its main board is a V707 evaluation board mounted with a QSFP board. The Prometeo's prototype has been designed and assembled by the University of the Witwatersrand and installed at CERN for further developing, tuning and tests. A presentation will be made of the design of the Prometeo, with particular emphasis on its firmware development.

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No

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d-br>&nbsp;(Hons, MSc, <br>> &nbsp; PhD)?

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Would you like to <br > submit a short paper <br > for the Conference <br > Proceedings (Yes / No)?

Yes

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