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FPGA-based emulation of qudit quantum Fourier transform circuit

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Abstract content
 (Max 300 words)

Quantum computing is the exploitation of quantum mechanical concepts such as entanglement and superposition to process quantum information. In some aspects of computing, quantum computing is known to outperform its classical counterpart. However, a full-scale quantum computer has not yet been realised. Therefore, in order to gain a deeper understanding of quantum circuits; which are building blocks of quantum computers, they are modelled either in software or hardware. Software simulation of quantum circuits is however limited, since it does not efficiently use parallelism, which is intrinsic in quantum computation. On the other hand, hardware-based emulation closely mimics quantum computation due to its parallelism, and the fact that emulation gives further insight on the issues of signal propagation delays and errors.

Currently, the emulation of quantum circuits has focused mainly on emulating two-dimensional quantum circuits [1-3]. In this paper, we report on the emulation of d -dimensional Quantum Fourier Transform (QFT) circuit using Field Programmable Gate Array (FPGA). We also compare the performance between software-based simulation and FPGA- based emulation.

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